

Queensland Open - 2020 State Titles

Doubles Open Mixed Event

Main Draw

		[1] Matthew HOPKIN [1] Jenny DUNCALF			
		A1	[1] HOPKIN / DUNCALF		
		BYE			
GTSC		[9] Zach HILFSTEIN	11-4, 11-3 (8m)	[1] HOPKIN / DUNCALF	
GTSC		[9] Ariel LYON			
NSC		11-3, 7-11, 11-7 (16m)	[9] HILFSTEIN / LYON		
NSC		[8] Josh WATERS			
		[8] Sophie FADAELY			
PSA		[5] Steve FINITSIS	11-5, 11-5 (15m)	[1] HOPKIN / DUNCALF	
PSA		[5] Kurstyn MATHER			
		A3	[5] FINITSIS / MATHER		
		BYE			
LCSC		[4] David WHITE	9-11, 7-11 (14m)	[4] WHITE / BOSWELL	
LCSC		[4] Vicky BOSWELL			
		A4	[4] WHITE / BOSWELL		
		BYE			
SSCAU		[3] Jason VAN DER WALT	11-9, 5-11, 11-7 (40m)	[1] HOPKIN / DUNCALF	
SSCAU		[3] Samantha FOYLE			
		A5	[3] VAN DER WALT / FOYLE		
		BYE			
GTSC		[6] David TURNER	11-2, 11-3	[3] VAN DER WALT / FOYLE	
GTSC		[6] Zoe AUST			
BSC		11-7, 11-4 (11m)	[6] TURNER / AUST		
BSC		[11] Corey RICHTER			
		[11] Tasmin SWAN			
SSCAU		[10] Miles TILL	8-11, 6-11	[2] TILL / GRINHAM	
SSCAU		[10] Heather PILLEY			
AHG		11-8, 11-7	[10] TILL / PILLEY		
AHG		[7] Ben HARRIS			
		[7] Samantha CALVERT			
		BYE	10-12, 4-11	[2] TILL / GRINHAM	
RP		A8	[2] TILL / GRINHAM		
RP		[2] Jordan TILL			
		[2] Rachael GRINHAM			

Position 5-8

	[9] HILFSTEIN / LYON			
	2-11, 9-11	[5] FINITSIS / MATHER		
	[5] FINITSIS / MATHER			
	[6] TURNER / AUST	5-11, 11-6, 11-6 (19m)	[5] FINITSIS / MATHER	
	9-11, 8-11	[10] TILL / PILLEY		
	[10] TILL / PILLEY			

The diagram illustrates a network of relationships between four entities (E1, E2, E3, E4) and three sets of names. The connections are as follows:

- E1** is connected to **[8] WATERS / FADAELY** (top left).
- E2** is connected to **[8] WATERS / FADAELY** (middle left).
- E3** is connected to **[11] RICHTER / SWAN** (bottom left).
- E4** is connected to **[7] HARRIS / CALVERT** (bottom left).
- [8] WATERS / FADAELY** is connected to **[8] WATERS / FADAELY** (top right).
- [11] RICHTER / SWAN** is connected to **[7] HARRIS / CALVERT** (bottom right).
- [7] HARRIS / CALVERT** is connected to **[7] HARRIS / CALVERT** (far right).

Additional labels include **BYE** and **[7] HARRIS / CALVERT** (middle right).

The diagram illustrates a 4-bit bus system with two processors. Processor 1 (left) sends data G1 and G2, while Processor 2 (right) sends data G3. The bus state is shown as a horizontal line at the bottom, which is initially high (idle). When Processor 1 sends G1, the bus goes low. When Processor 2 sends G3, the bus goes high. When Processor 1 sends G2, the bus goes low. The diagram shows the sequence of data transfers and the resulting bus state.